

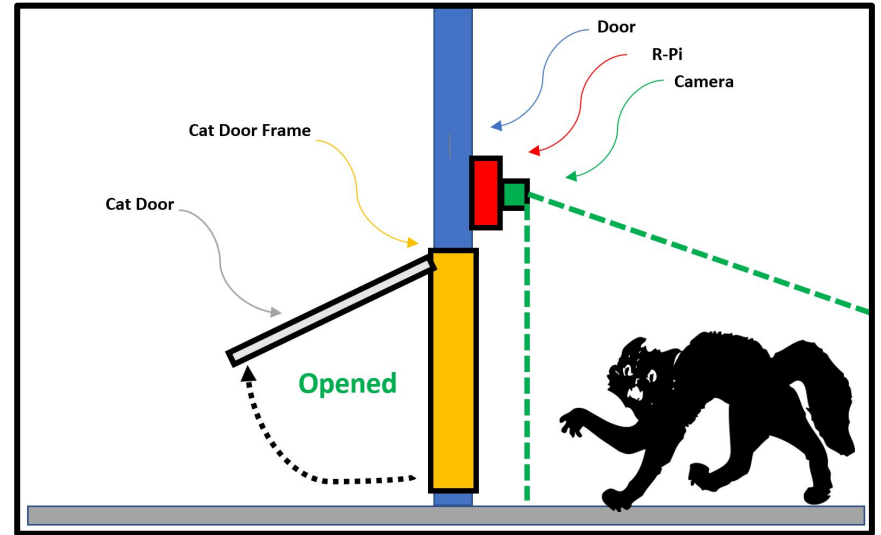
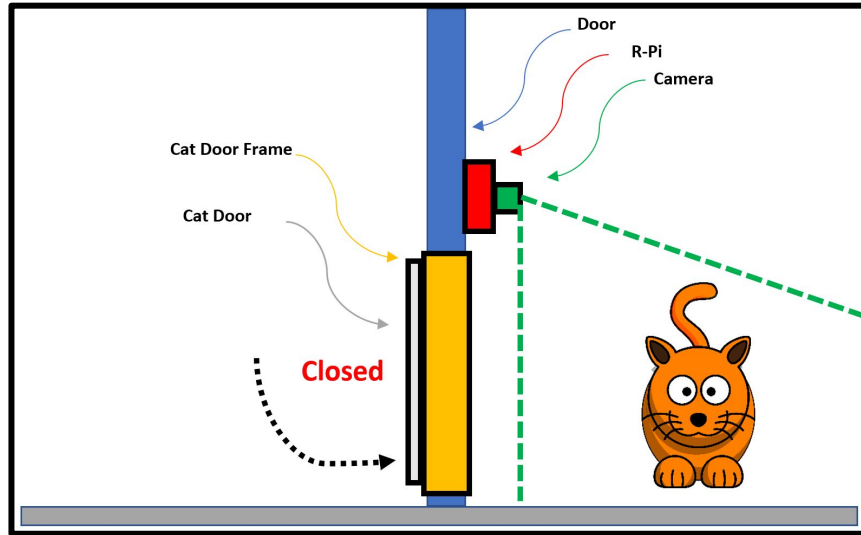
ECE 473 Final Project: CatNet

(a smart cat door)



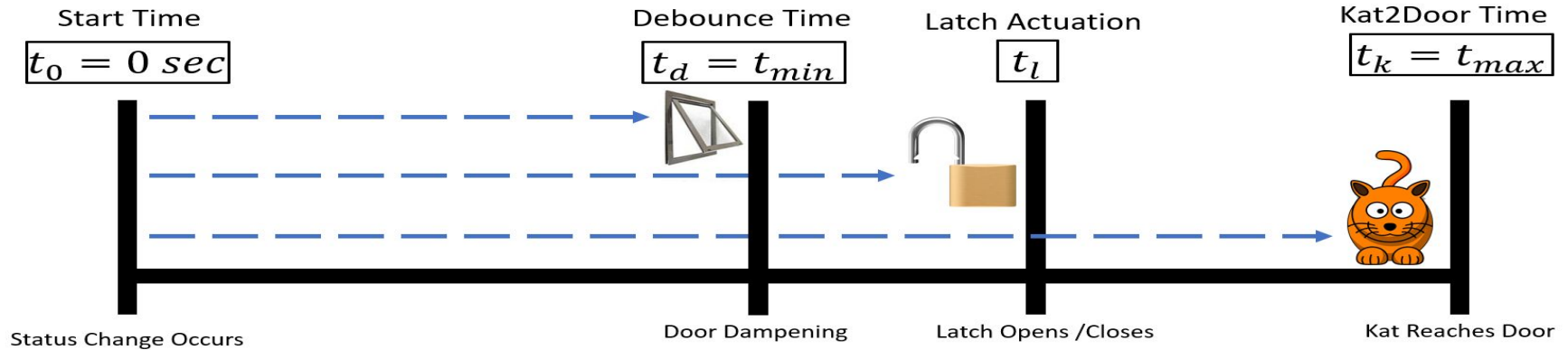
By: Desmond Carter & Roderick Renwick

Concept



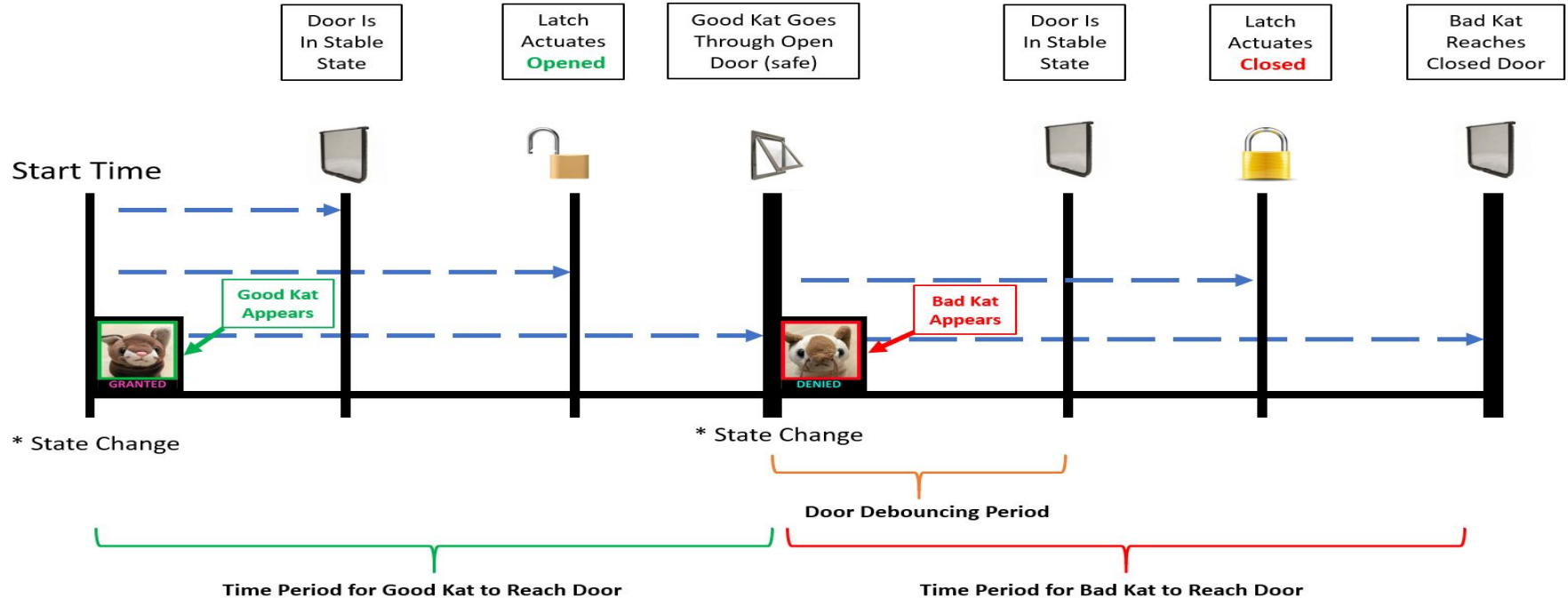
Requirements

Time Constraint: $t_{min} < t_l < t_{min}$

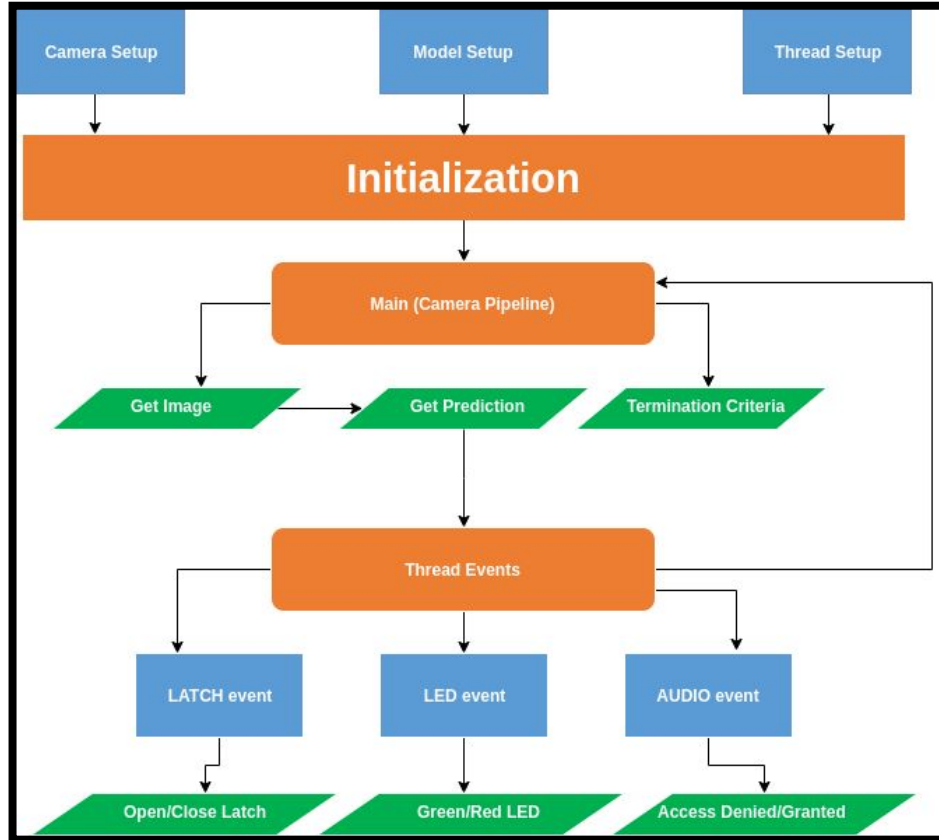


Requirements

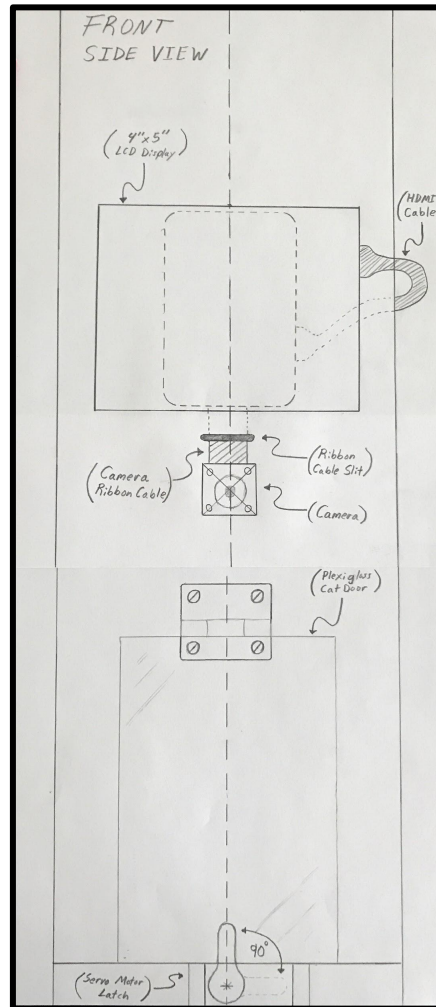
Time Constraint Example



Design: System Flowchart



Design: Physical Structure



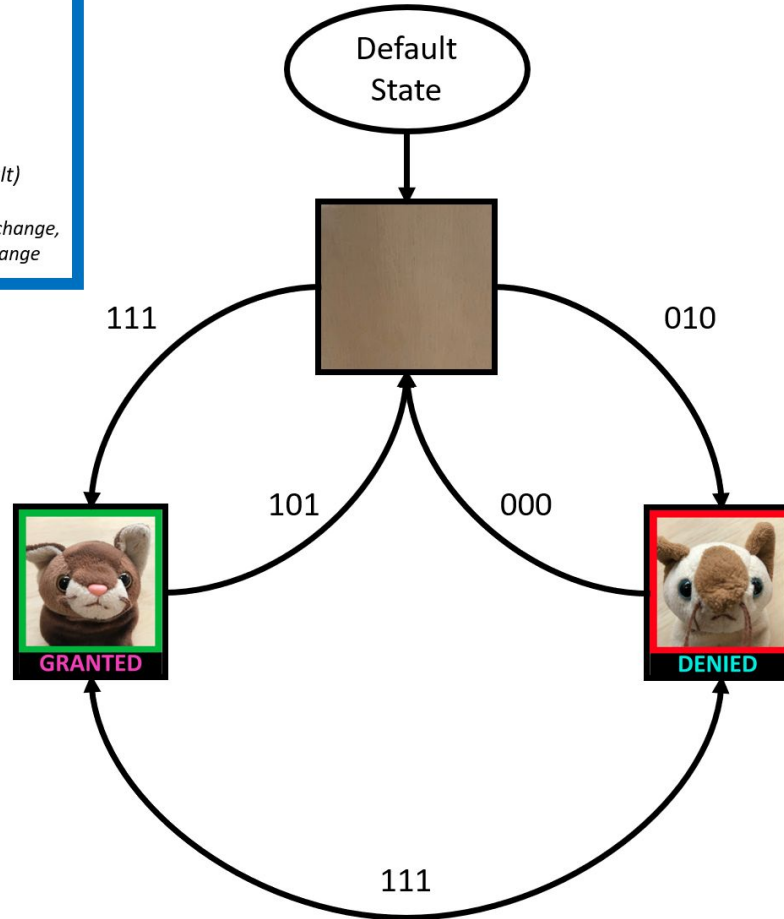
Design: Moore State Change Diagram

LEGEND:

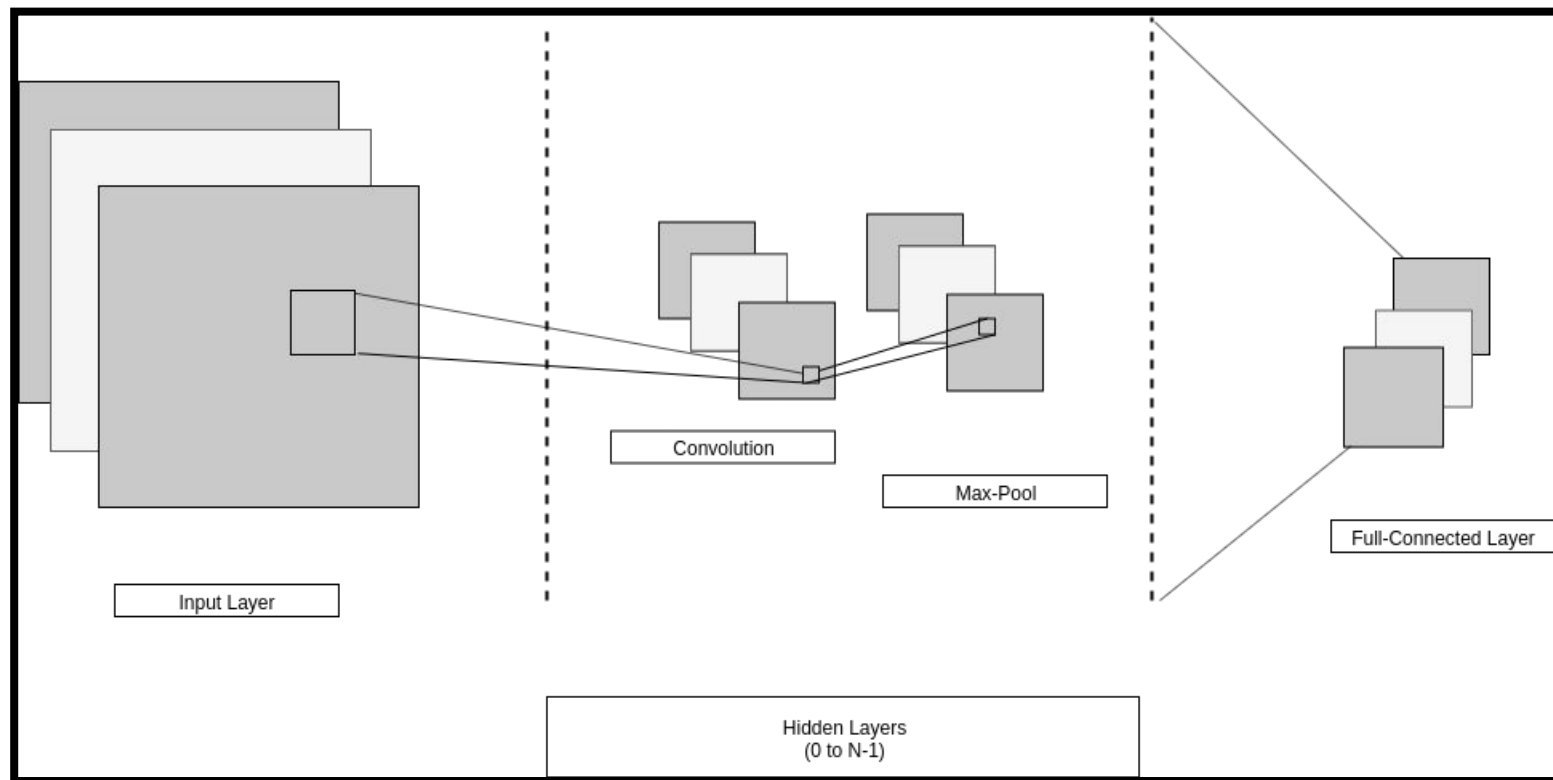
B0 : Latch State
B1 : Audio State
B2 : LED State

EX: B0, B1, B2 = "000" (default)

* **NOTE:** A "1" indicates a state change,
while a "0" indicates no state change



Design: CNN Model



Implementation

